

SEC3IO / SEC3IO-SA

Datasheet



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Processor / Memory / Mass Storage

CPU	ARM Cortex-A8 1 GHz
RAM	256 MB DDR3L
Flash	512 MB SLC NAND

Power Supply

Voltage	$U_{PWR1/2}$: 12 / 24 / 48 V DC (9 – 60 V DC)
Power consumption	Max. 10 W (typ. 4 W without USB)
Line cross-section	0.129 – 3.31 mm ² (AWG 26...12, solid or stranded wire)
Features	Redundant power feed with fault contact "FAIL"

Interfaces

Ethernet interface	2x RJ45 10/100BASE-T
Serial interface	2x RJ45 RS232 / RS422 / RS485 Baud Rate: 300 – 115200 Baud
USB interface	2 x USB 2.0 up to 480 Mbps "high speed"
State relay "FAIL"	Maximum switching power: 60 W, 125 VA Maximum switching voltage: 220 V DC, 250 V AC Maximum carrying current: 2 A Maximum switching current: 2 A
Digital Input	8x Digital Input Input voltage: 0 – 24 V DC (U_{DI1-8_max} : 30 V DC) Input level low: ≤ 2.96 V DC $\pm 1\%$ Input level high: ≥ 3.49 V DC $\pm 1\%$ Input impedance: 1.28 M Ω $\pm 2\%$ Sampling rate: max. 500 Hz $\pm 5\%$ Line cross-section: 0.129 – 3.31 mm ² (AWG 26...12, solid or stranded wire)
Digital Output	8x Digital Output (MOSFET P-Channel high side) Input voltage V_{SO} : 9 – 24 V DC - U_{VSO_max} : 30 V DC - I_{VSO_max} : 4 A Output voltage: $U_{DO1-8} = V_{SO} - \{0.4 \text{ V @ } 0 \text{ A .. } 1 \text{ V @ } 0.5 \text{ A}\}$ Max. output current: 500 mA Max. switching frequency: $t_{impulse} \leq 2$ kHz $\pm 25\%$ Overcurrent protection - switchover to high-impedance state in the event of a fault: - if 500 mA limit is exceeded - when switching on load ≥ 350 mA - switch-off time in case of error: 65 μ s $\pm 5\%$ (with 22 Ω and $U_{VSO} = 24$ V DC) - deactivation switch-off of outputs can be reset by software Line cross-section: 0.129 – 3.31 mm ² (AWG 26...12, solid or stranded wire)

Diagnostics (Status LEDs)

PWR	Power LED
USR	LED freely configurable by software
CPU	LED to show different software conditions
COM1 / COM2	Send and receive LED for serial interfaces
ETH0 / ETH1	Link and activity LED for Ethernet interfaces
DI 1 – 8	Status LED for digital input <i>(green if high level)</i>
DO 1 – 8	Status LED for digital output <i>(green if output active; red if output deactivated due to error)</i>

Additional Functions and Features

Battery buffered real time clock	Supported by a lithium battery (CR2032)
State relay "FAIL"	Changeover switch controlled by software
Hardware watchdog	
Temperature monitoring	
Power supply monitoring	
Overvoltage protection	The power supply and all interfaces are ESD, surge, and burst protected (see EMC)

Housing

Body material	Steel chassis
Mounting	35 mm DIN-Rail
IP Code	IP30
Rotating parts	None
Dimensions (W x H x D)	approx. 65 mm x 124 mm x 139 mm
Weight	approx. 0.65 kg

Operating Environment

Operating temperature	-20 °C to 60 °C
Storage temperature	-40 °C to 85 °C
Relative humidity	5% to 95% not condensing

Approval, Standards and Conformity

Approval	CE (Industrial)
Standards	EN 61000-6-2: 2005 EN 61000-6-3: 2007+ A1:2011 EN 61850-3: 2014 ¹⁾ (only SEC3IO-SA)
Conformity	RoHS REACH WEEE

Electromagnetic Compatibility (EMC) – Emission Requirements

EN 55016-2-1:2014	Conducted emission on power supply lines in the frequency range 150 kHz - 30 MHz
EN 55016-2-1:2014	Conducted emission on telecommunication lines in the frequency range 150 kHz - 30 MHz
EN 55016-2-3:2010 + A1:2010 + AC:2013 + A2:2014	Radiated emission in the frequency range 30 MHz – 1 GHz
EN 55016-2-3:2010 + A1:2010 + AC:2013 + A2:2014	Radiated emission in the frequency range 1 GHz – 6 GHz

Electromagnetic Compatibility (EMC) – Immunity Requirements

EN 61000-4-2: 2009	Electrostatic discharge (ESD) - Contact discharge ± 6 kV - Air discharge ± 8 kV
EN 61000-4-3: 2006 + A1:2008 + A2:2010	Immunity to RF electromagnetic fields in the frequency range 80 – 3000 MHz, Test level 10 V/m
EN 61000-4-4: 2012	Immunity to fast transients (Burst) - DC power port ± 2 kV - Signal lines ± 2 kV
EN 61000-4-5: 2014	Immunity to surges on power supply lines (Surge) - DC power port: line \leftrightarrow ground ± 2 kV - DC power port: line \leftrightarrow line ± 1 kV
EN 61000-4-5: 2014	Immunity to surges on shielded signal lines (Surge) - Shielded lines ± 1 kV
EN 61000-4-5: 2014	Immunity to surges on unshielded signal lines (Surge) - Unshielded lines ± 1 kV
EN 61000-4-6: 2014	Immunity to conducted interference induced by radio-frequency fields in the frequency range 150 kHz – 80 MHz, Test level 10 V
EN 61000-4-12: 1995 + A1:2001	Immunity to ring wave - Frequency 100 kHz / 1MHz - Power port DC ± 1.25 kV line to line - Data lines ± 2.5 kV line to ground

EN 61000-4-16: 1998 + A:2004 + A2:2011	Immunity to conducted interference induced by low-frequency fields - Short (1s) 16⅔ Hz / 50 Hz / 60 Hz – 100 V - Continuous 16⅔ Hz / 50 Hz / 60 Hz – 10 V - 15 Hz – 150 Hz: 10 to 1 V - 150 Hz – 1.5 kHz: 1 V - 1.5 kHz – 15 kHz: 1 to 10 V - 15 kHz – 150 kHz: 10 V
EN 61000-4-17: 1999 + A2:2009	Immunity to Ripple on DC input power port - DC 10% Un
EN 61000-4-18: 2007 + A1:2010	Immunity to damped oscillatory waves - Frequency 100 kHz / Repetition rate 40 s - Frequency 1 MHz / Repetition rate 400 s - Power port DC DC ± 1.25 kV line to line DC ± 2.5 kV line to ground - Data lines ± 2.5 kV line to ground
EN 61000-4-29: 2000 ¹⁾	Immunity to voltage dips and interruptions on DC input power port - 0 % Un on PWR1 – 0.05 s

SEC3IO-SA; IEC 61850-3; EN 61850-3: 2014

The SEC3IO-SA hardware model meets the special requirements for electromagnetic compatibility and power supply described in Part 3 of the IEC 61850 standard.

¹⁾ Only with the hardware model SEC3IO-SA.